



September 16-18, 2024 Santa Clara, CA

Problems with Al Interconnects?

Come yell at our Opinionated Experts!

The Opinionated Experts



Sr. Director NAND Product Planning – Standards, Samsung



Mechanical Engineer, Amphenol Co-Chair SFF TA SNIA TC



Director, SSD Industry Standards, Kioxia SNIA Board Member



Distinguished Member of the Technical Staff, Micron Co-Chair SFF TA SNIA TC



A Quick Note on SFF

76 member companies



150+ published specs



Specifications used everywhere



In the last <u>12 months</u>, we: <u>Published</u> 5 new specifications <u>Revised</u> 7 existing specifications



Basic Ground Rules

- This is an open panel. Please ask questions
- Topics we have strong opinions on:
 - Post PCIe Gen 6 cables and connectors
 - Higher storage capacities in form factors
 - Lane Bifurcation and lane counts with higher PCIe speeds
 - Retimers (Optical vs. electrical).
 - Transceivers at higher speeds (400+)



Join SFF to help create the next AI interconnects!

Resources:

- How to Join: https://www.snia.org/join
- Public Site: https://www.snia.org/sff
- Specifications: https://www.snia.org/sff/specifications
- Questions about membership? Please send mail to membership@snia.org
- Additional questions? Please send mail to <u>sff_ta_twgchair@snia.org</u>





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